

PRIOR ART

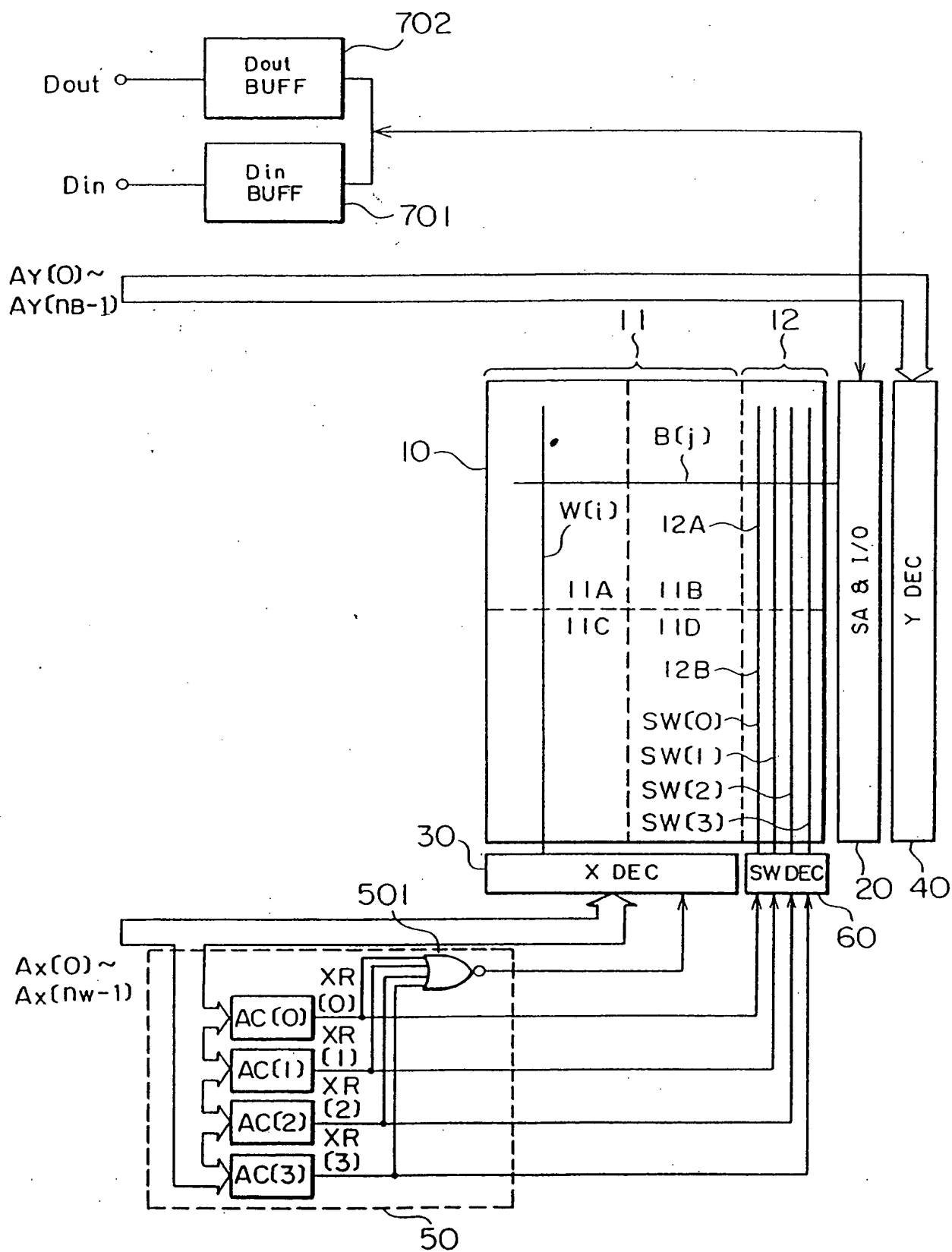


FIG. 1B
PRIOR ART

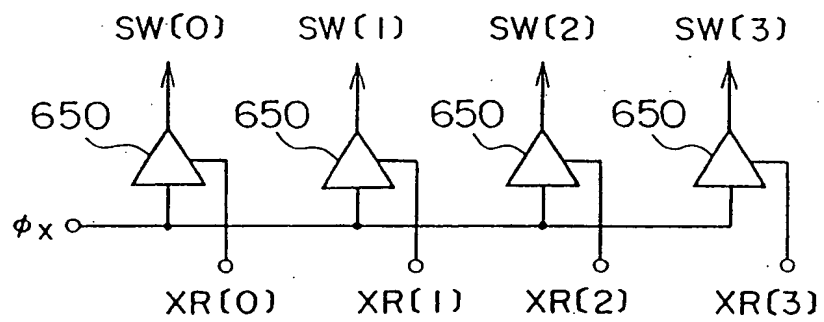


FIG. 2A
PRIOR ART

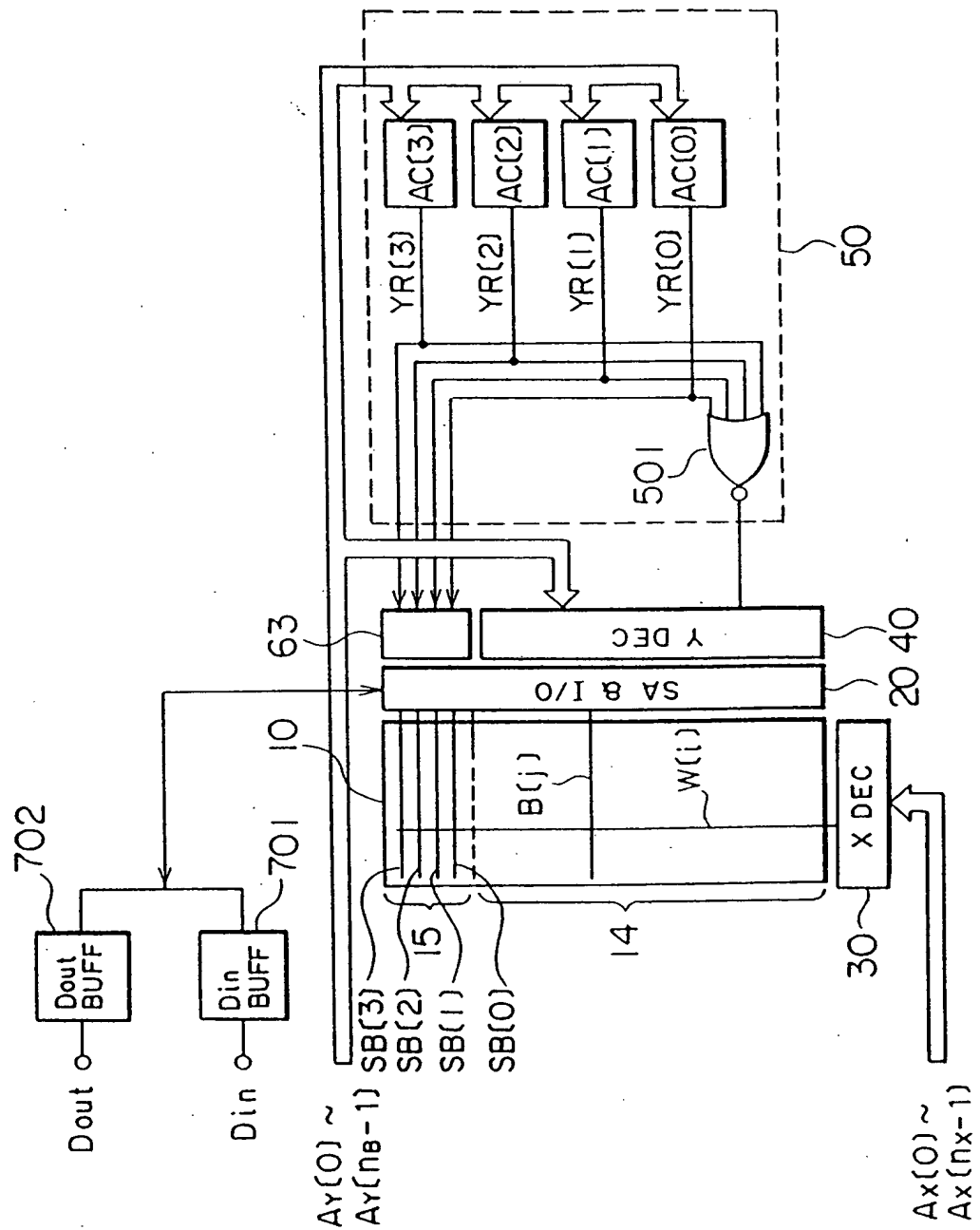


FIG. 2B
PRIOR ART

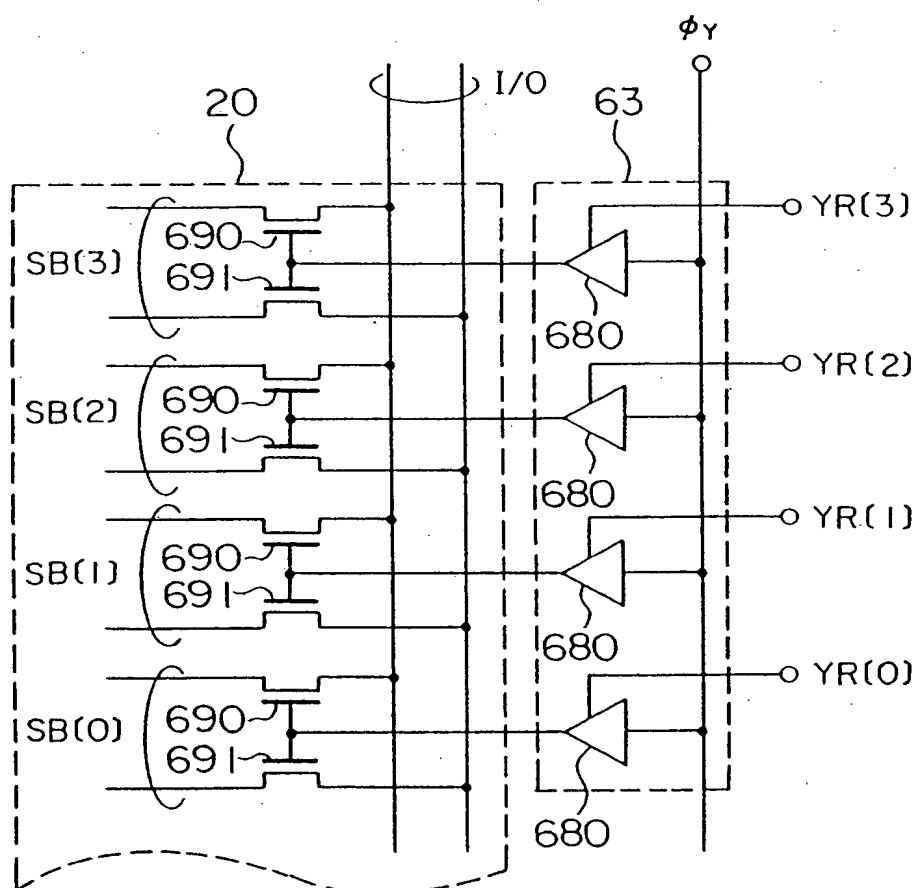


FIG. 3
PRIOR ART

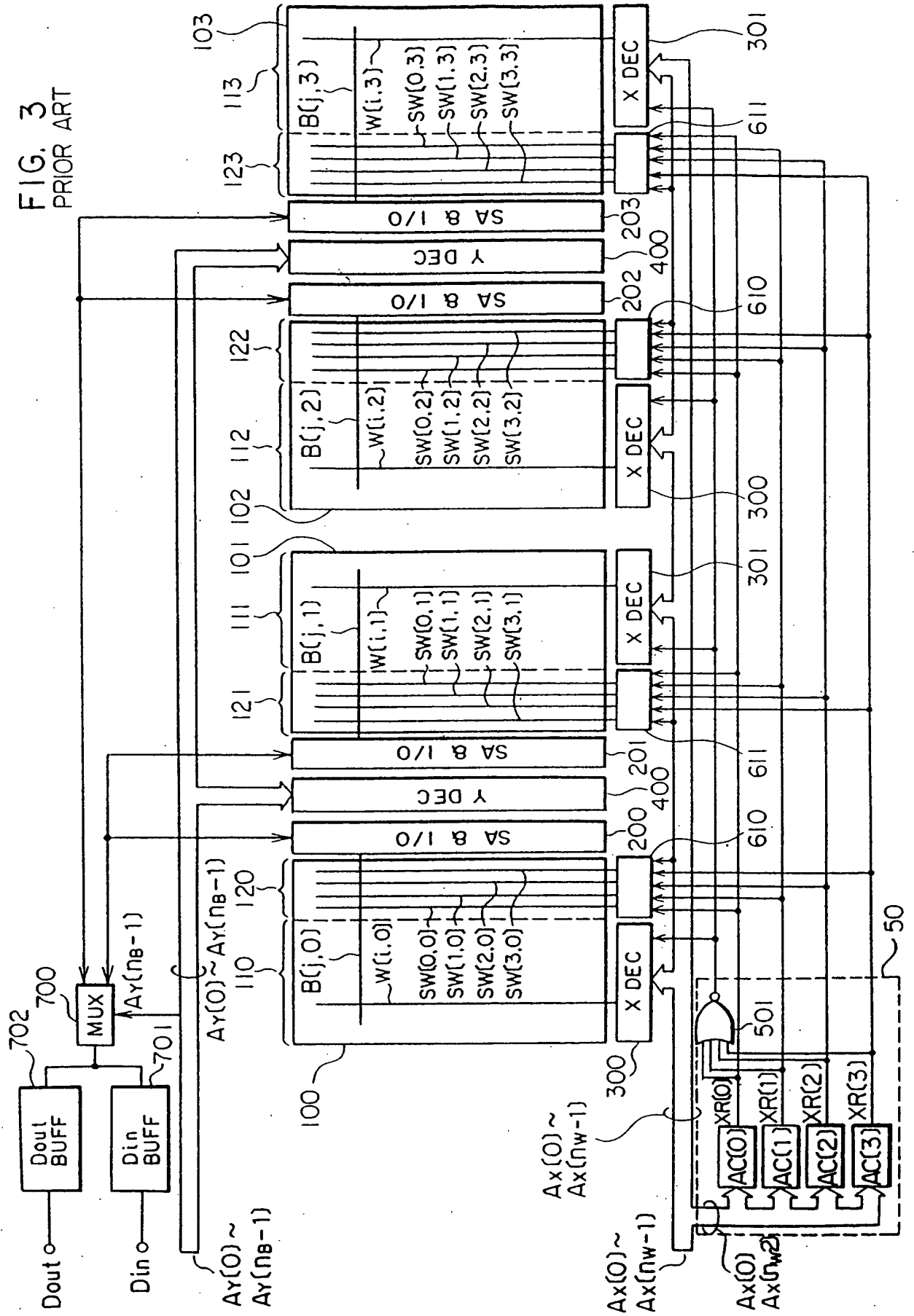


FIG. 4
PRIOR ART

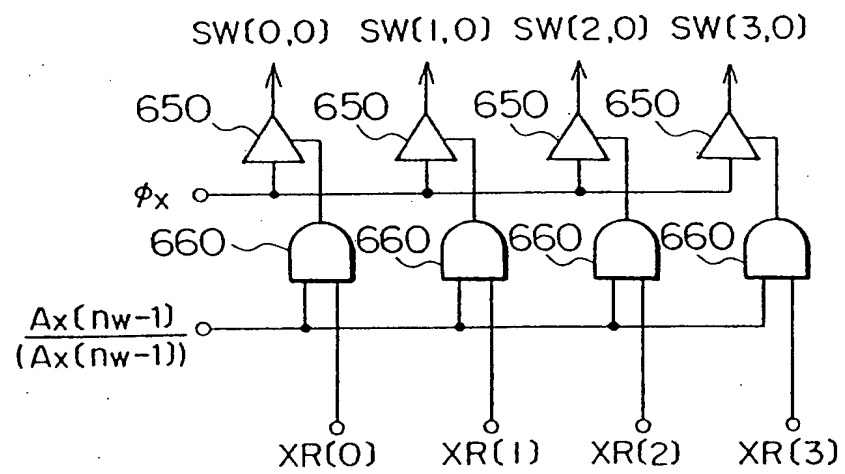


FIG. 5
PRIOR ART

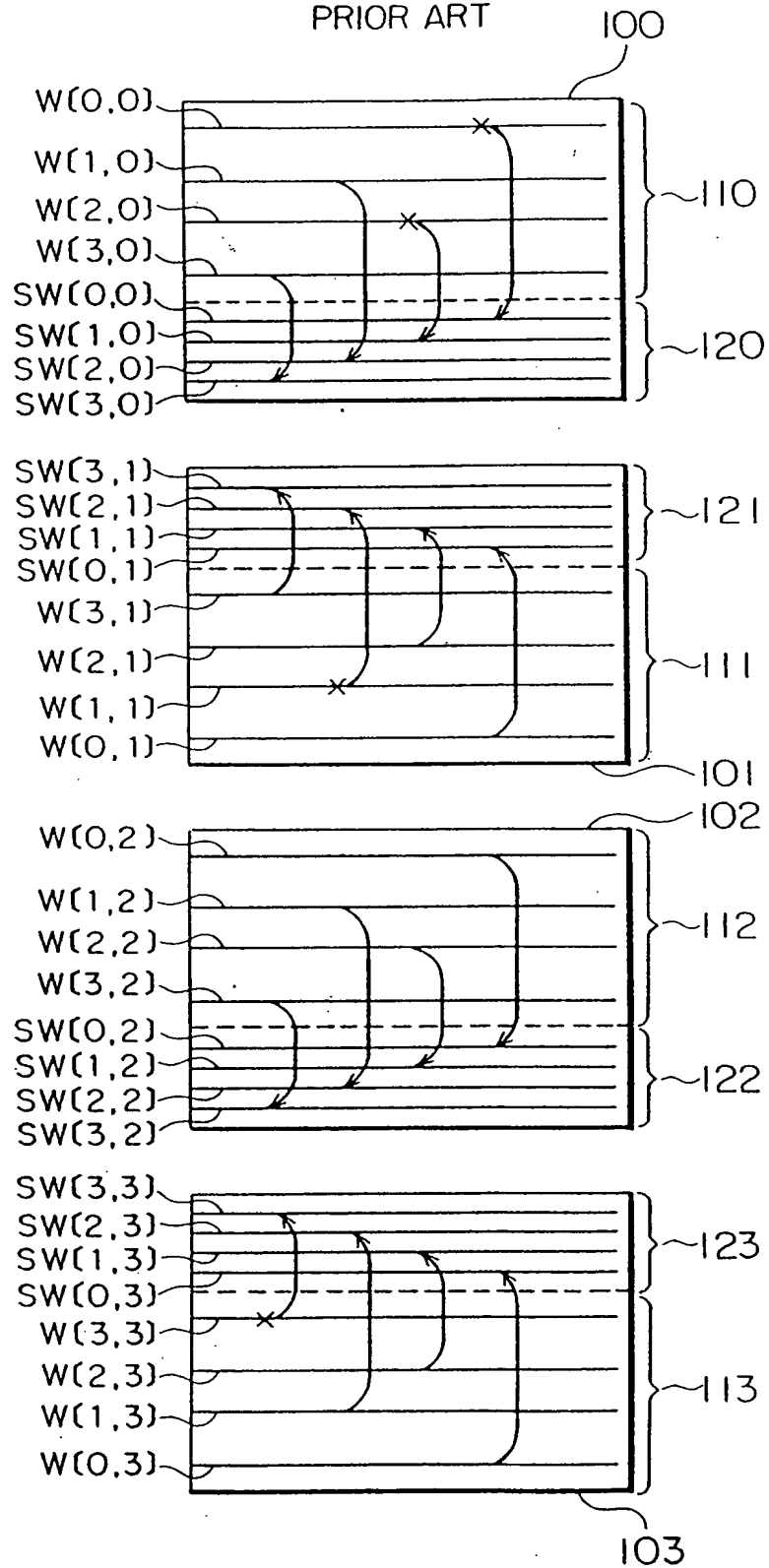


FIG. 6
PRIOR ART

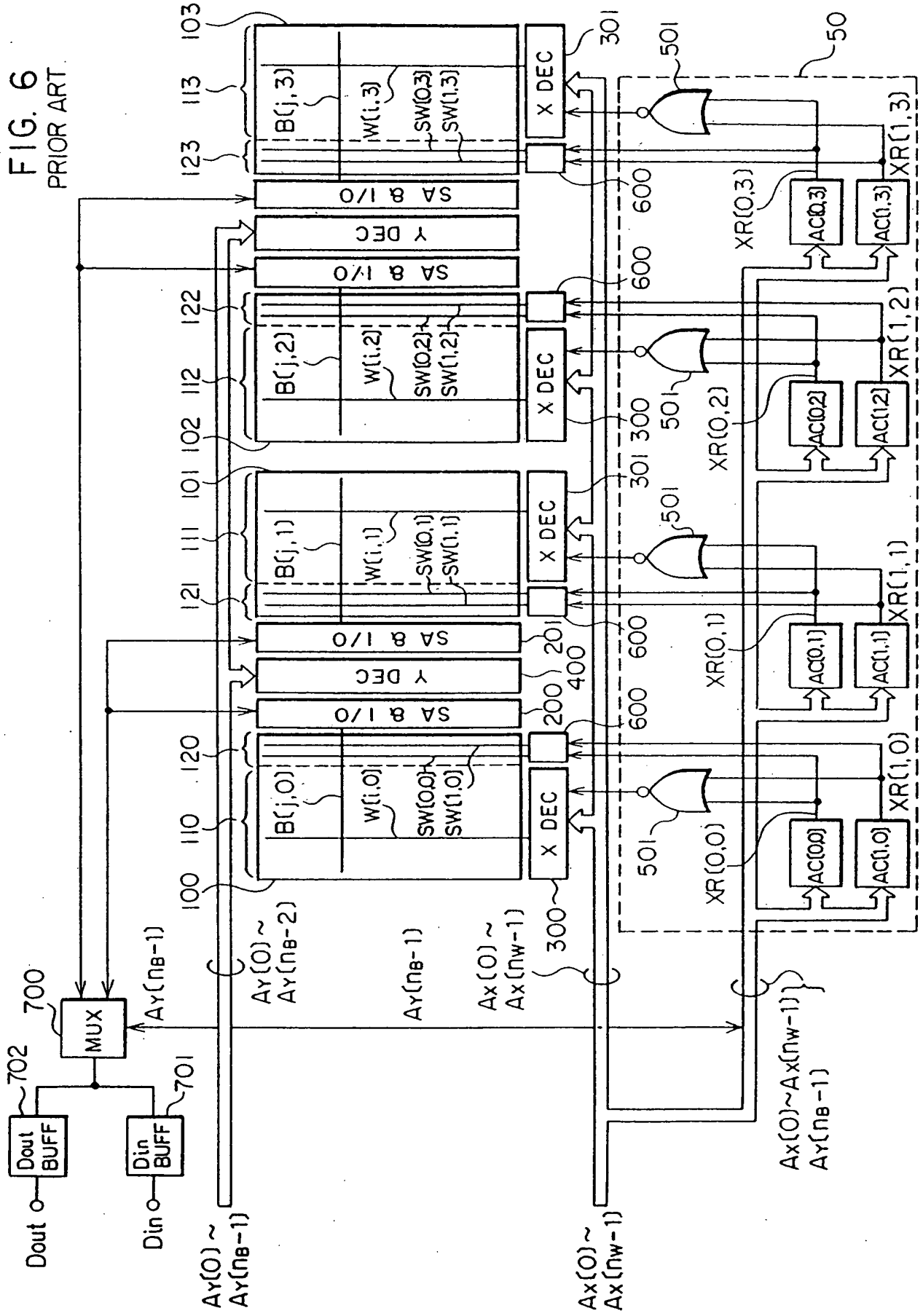


FIG. 7
PRIOR ART

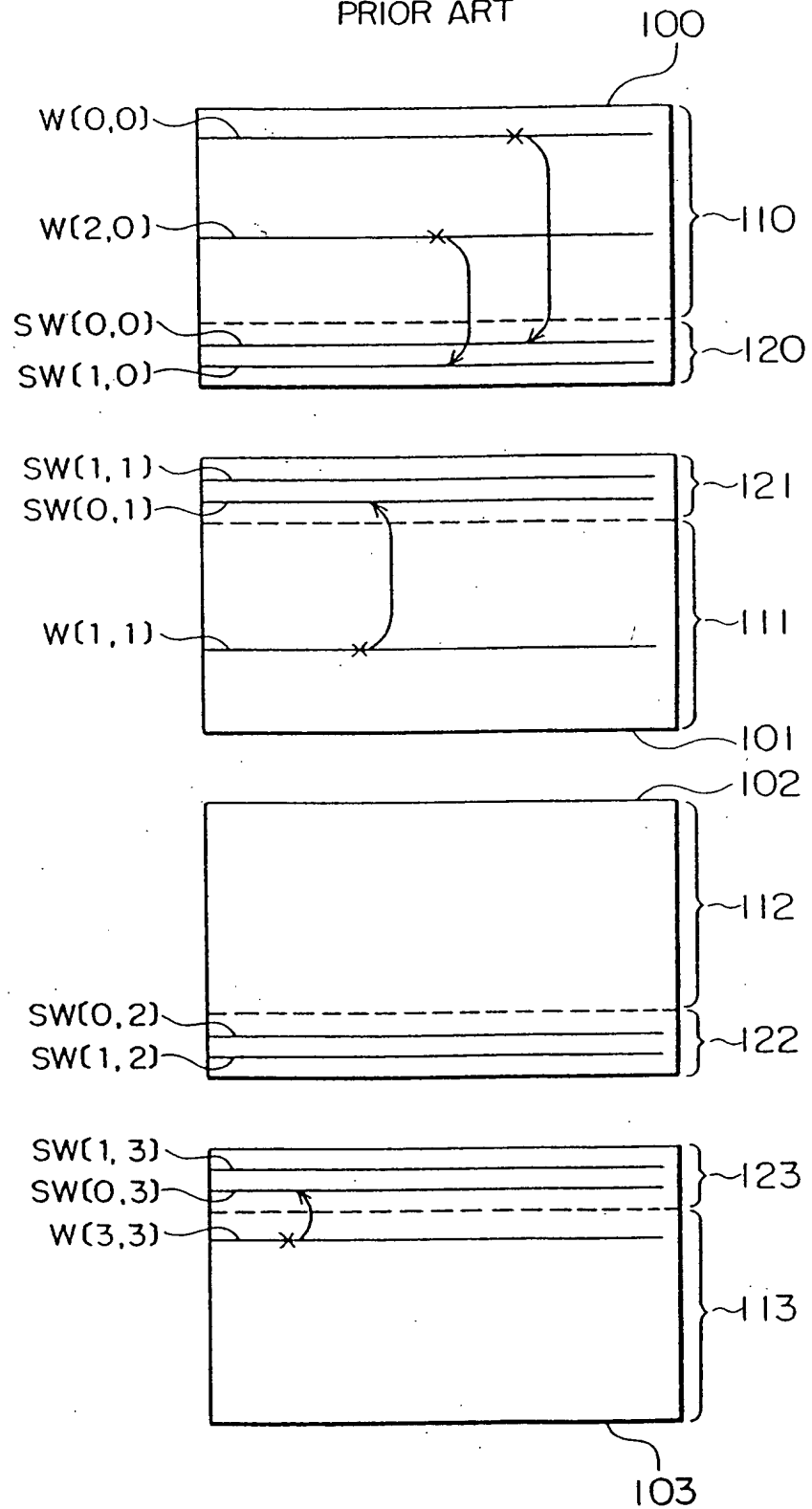


FIG. 8
PRIOR ART

VALUE STORED IN ADDRESS COMPARE CIRCUIT	INPUT ADDRESS	RESULT OF COMPARISON
" 0 "	" 0 "	COINCIDENT
	" 1 "	NOT COINCIDENT
" 1 "	" 0 "	NOT COINCIDENT
	" 1 "	COINCIDENT
" X "	" 0 "	COINCIDENT
	" 1 "	COINCIDENT

FIG. 9

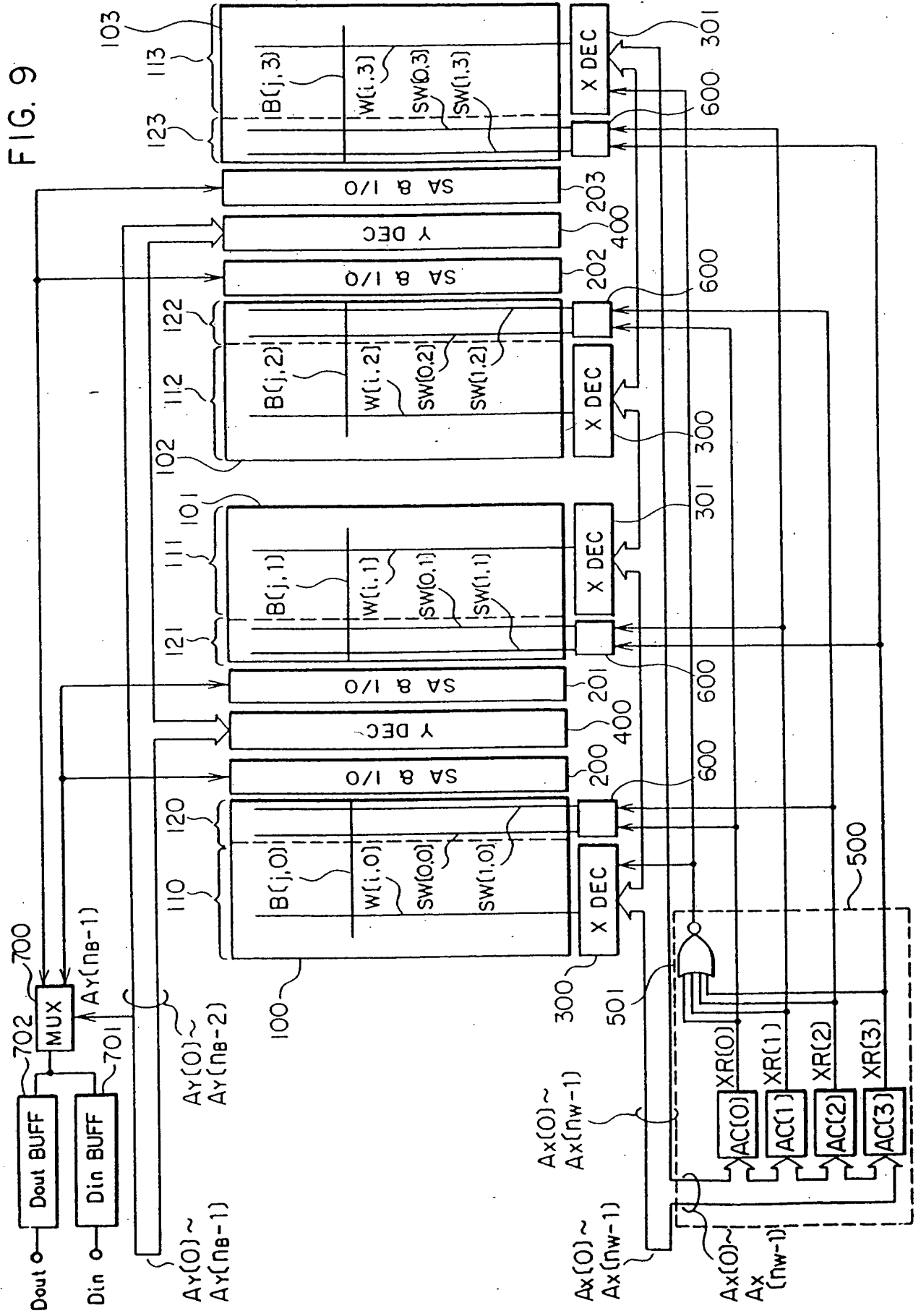


FIG. 10

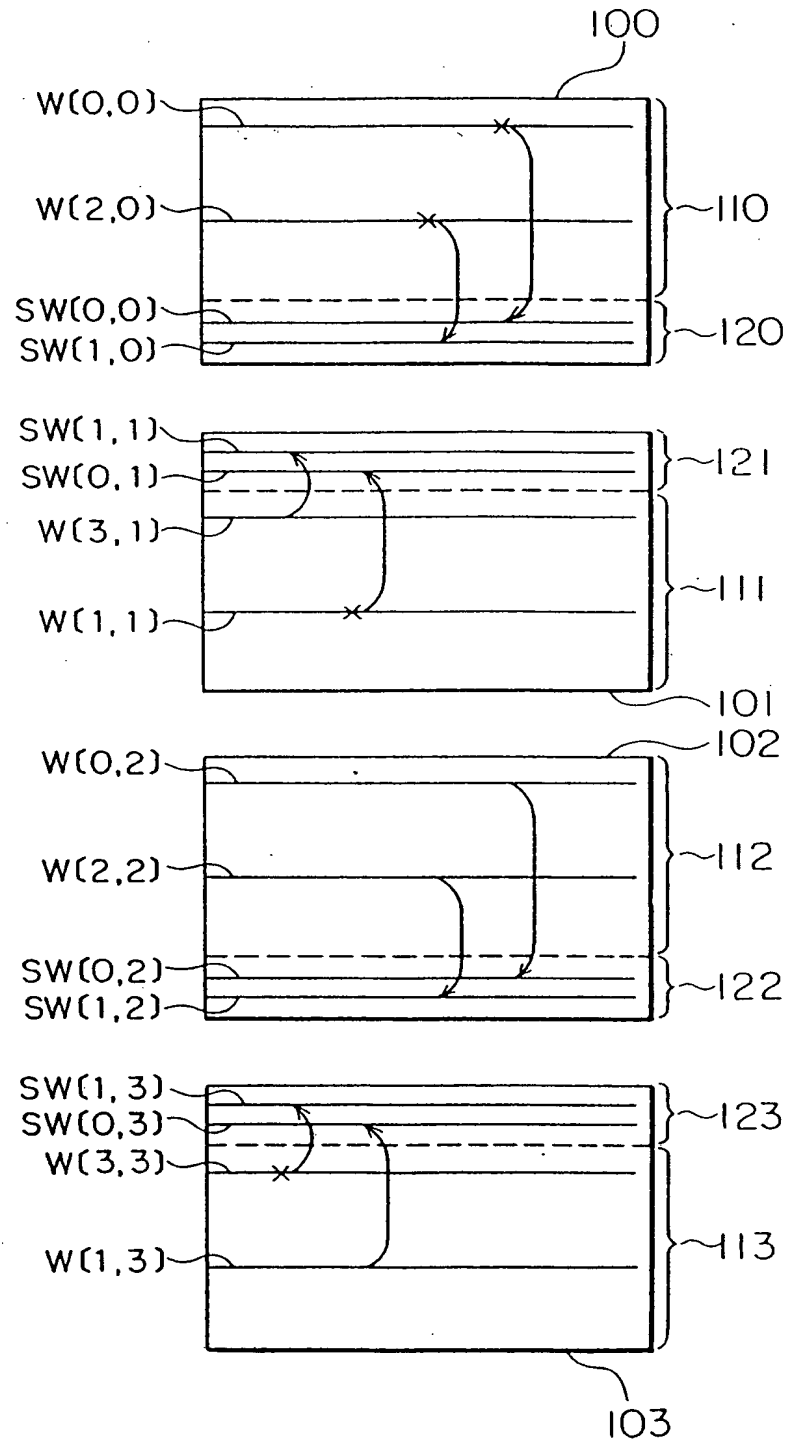


FIG. 11

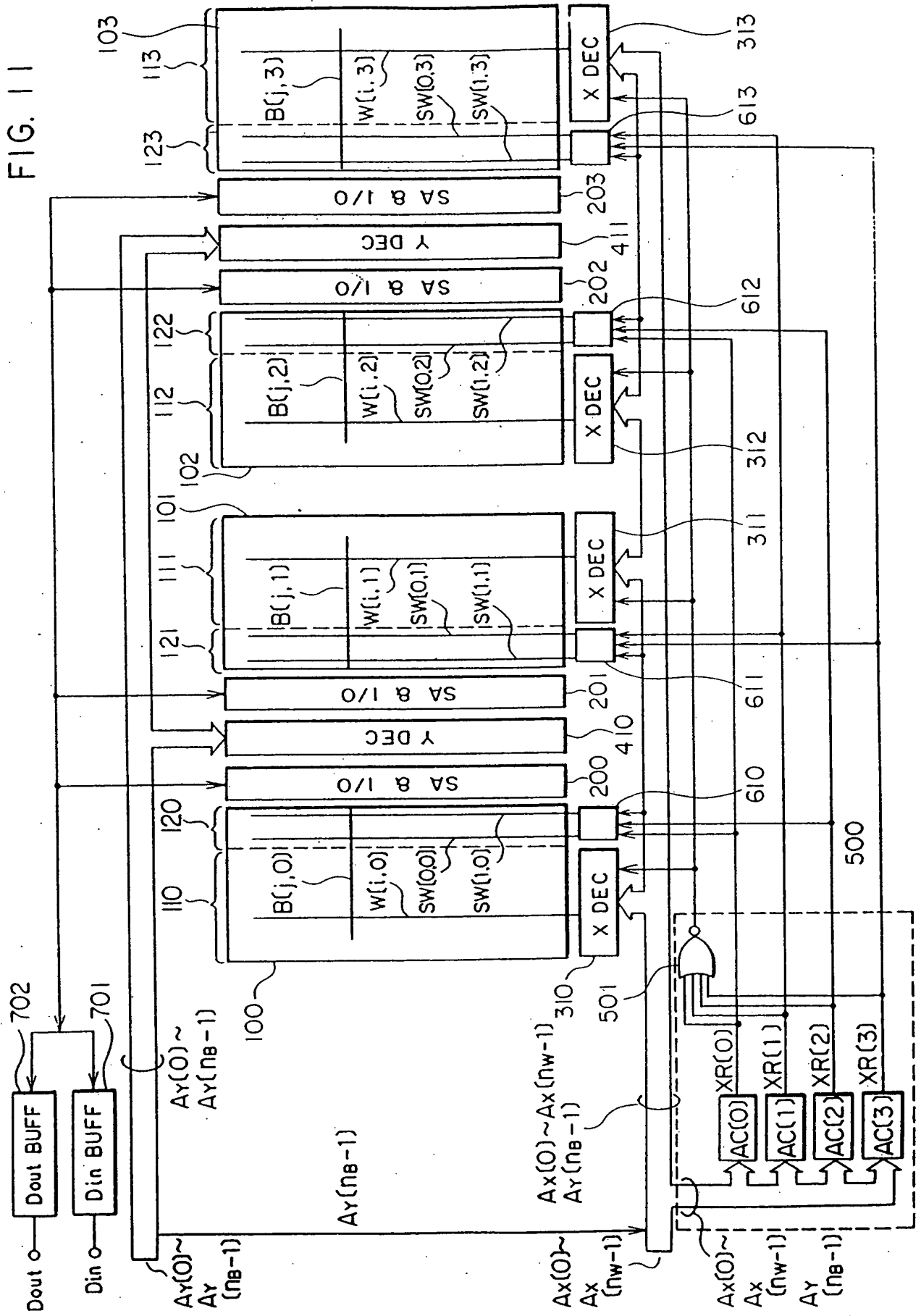


FIG. 12

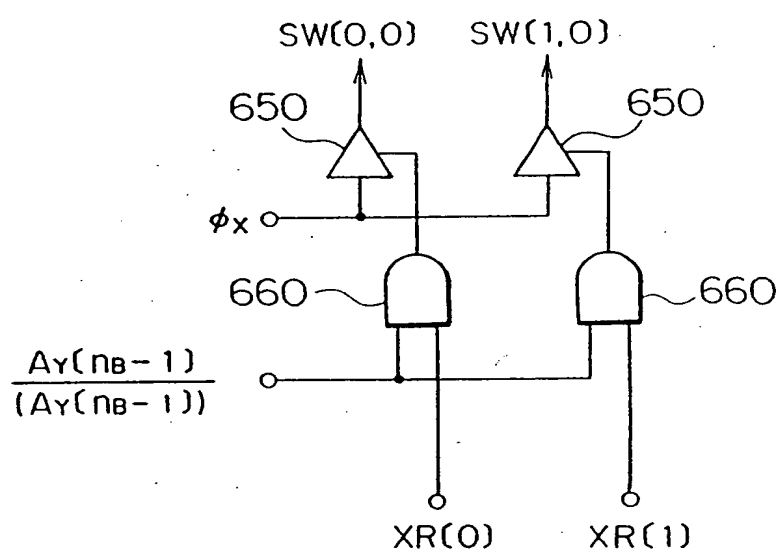
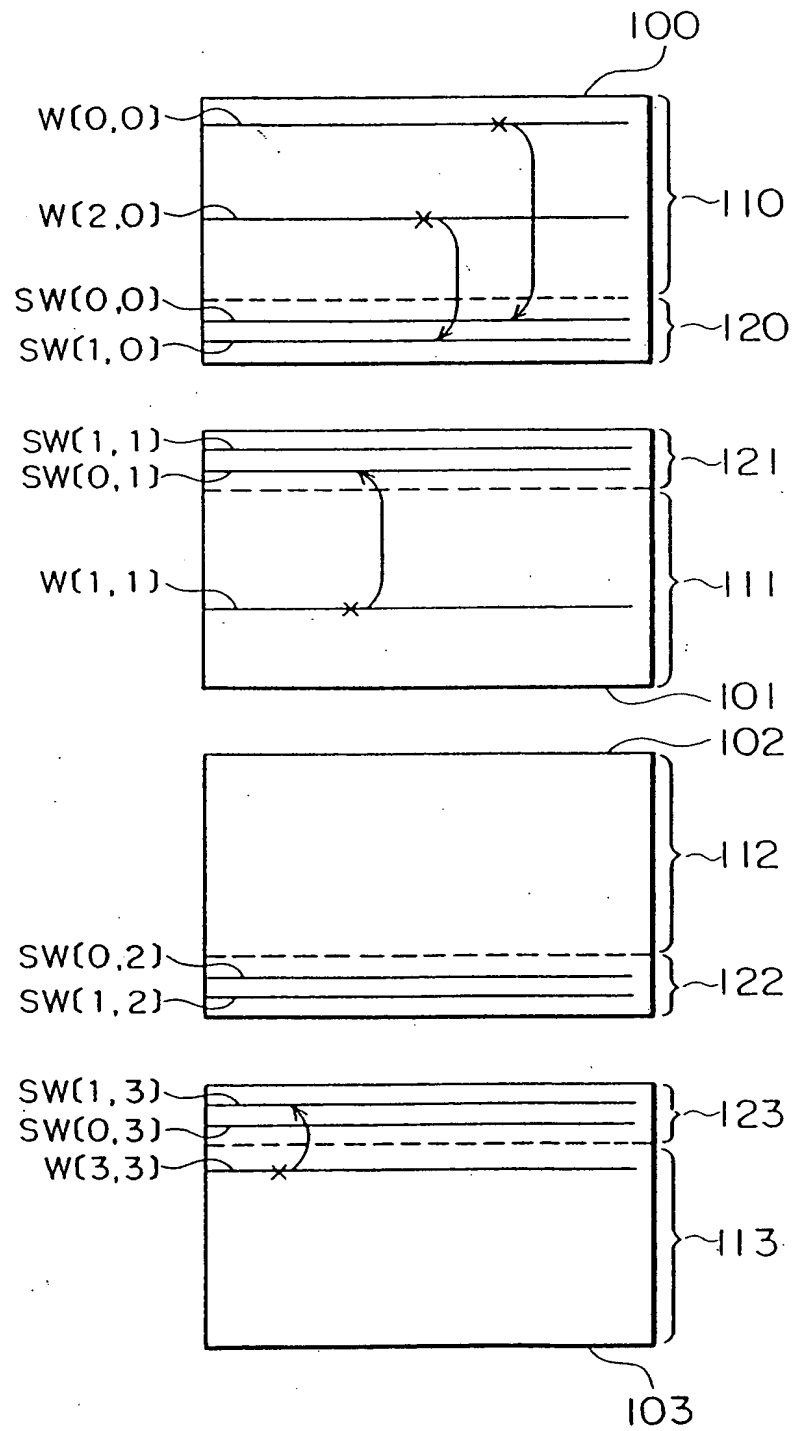


FIG. 13



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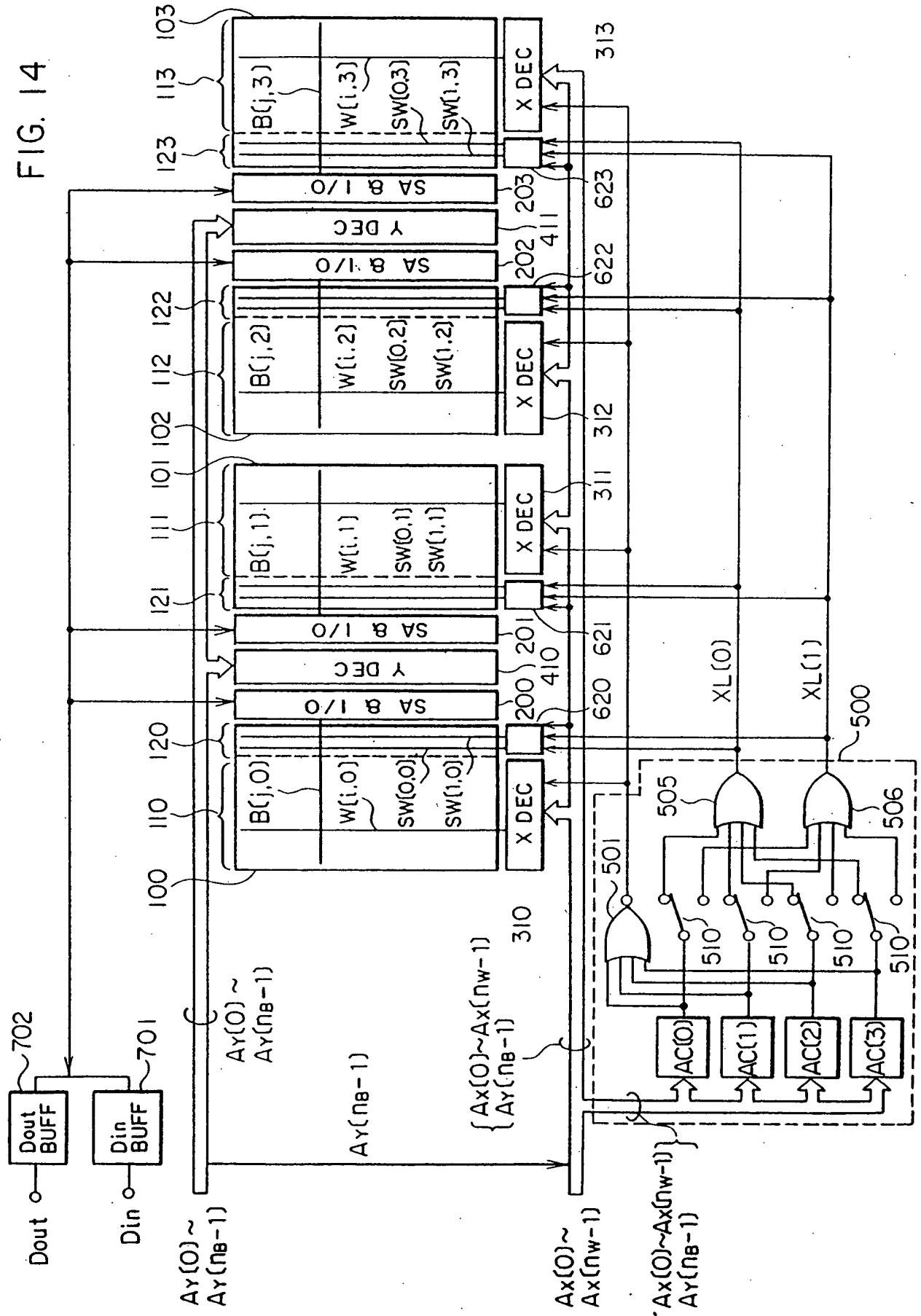


FIG. 15

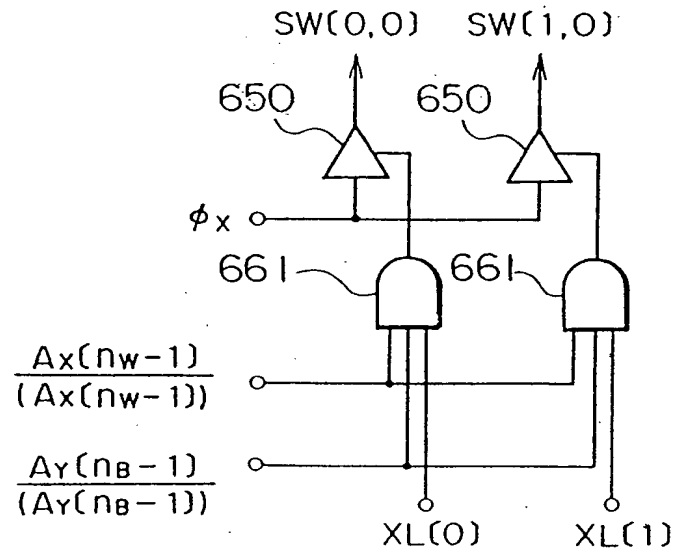


FIG. 16

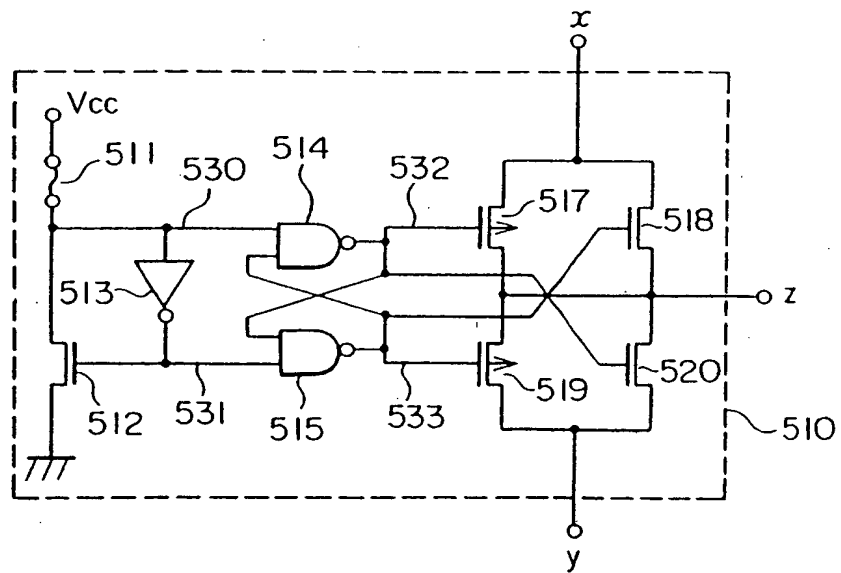


FIG. 17

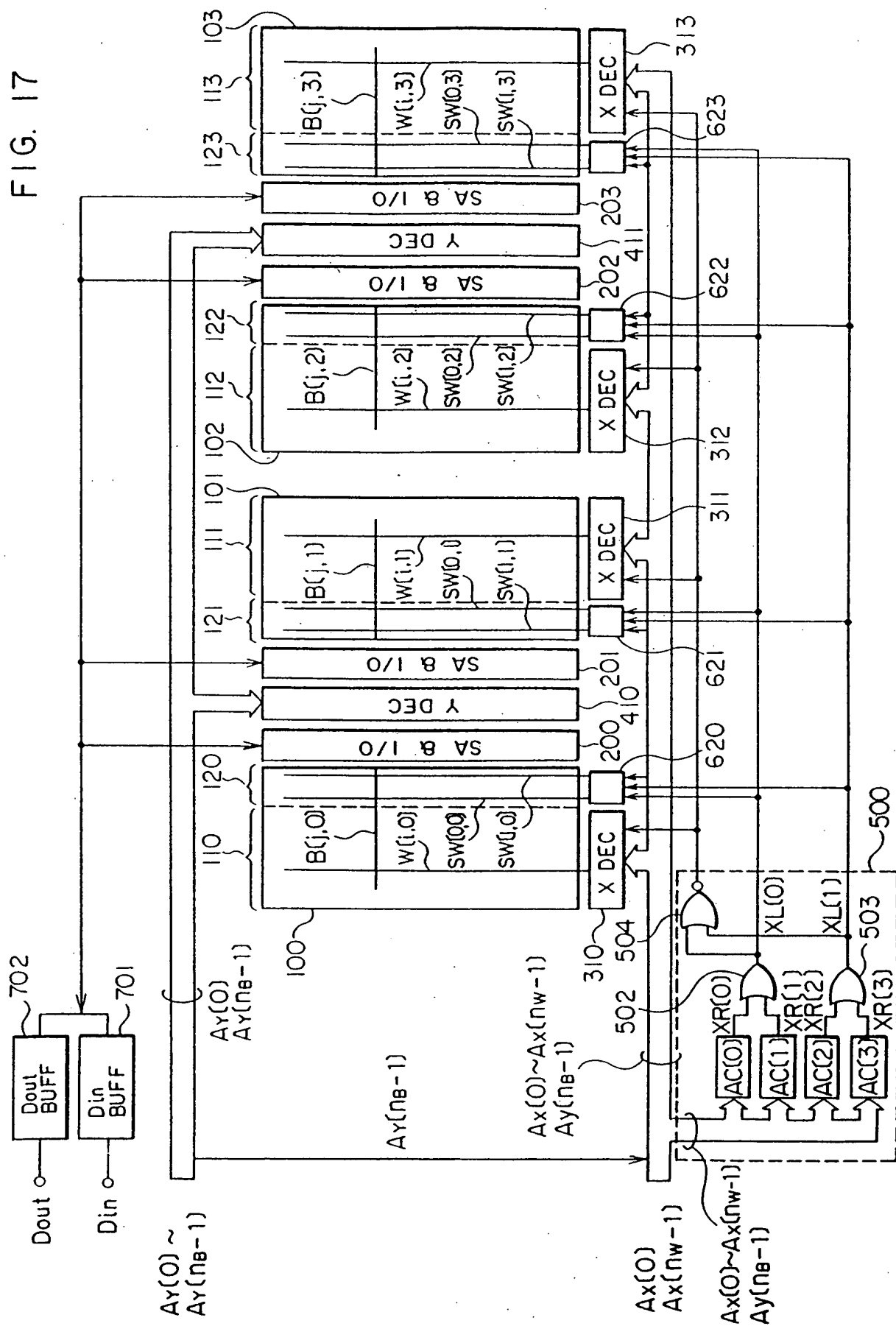


FIG. 18

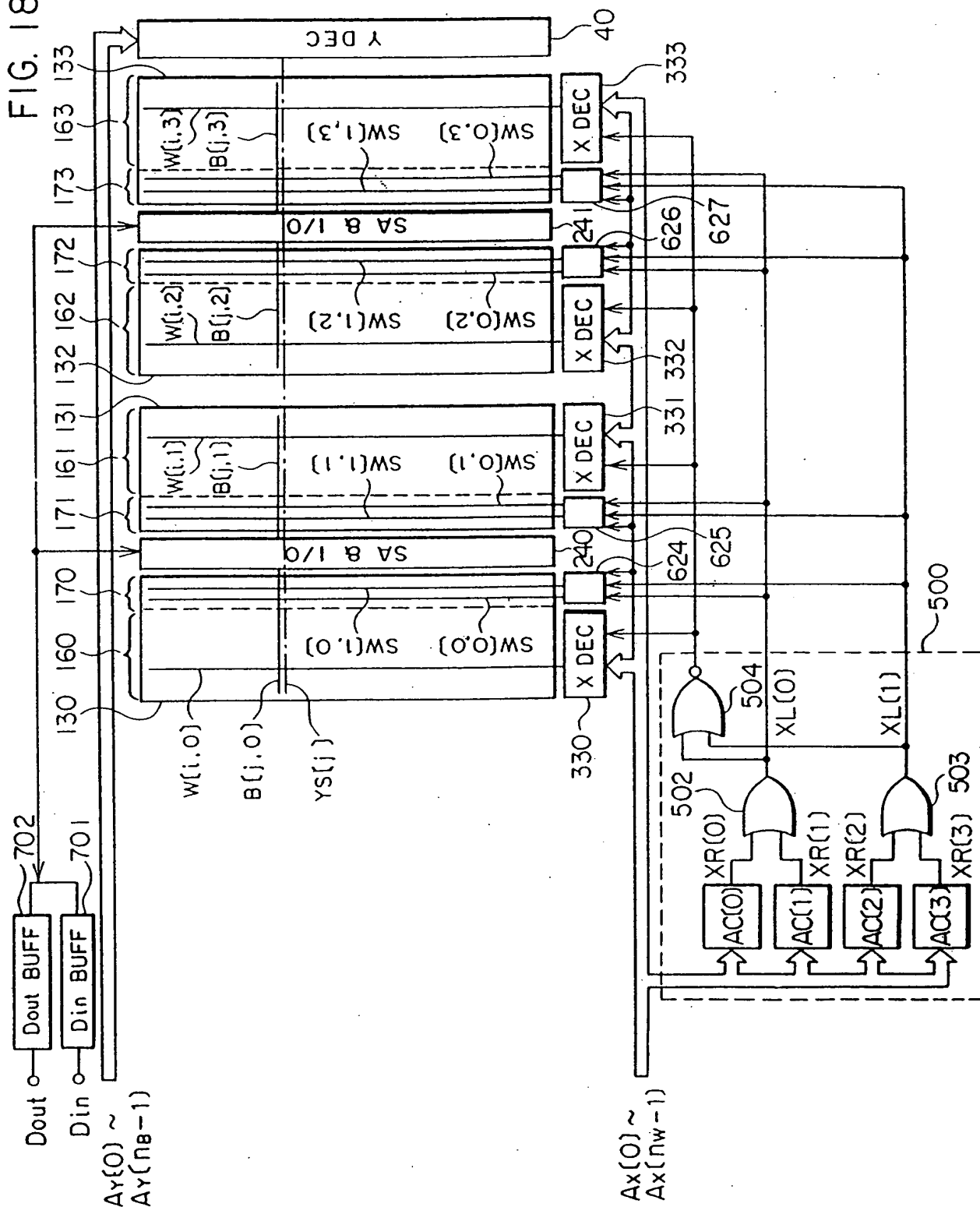


FIG. 19

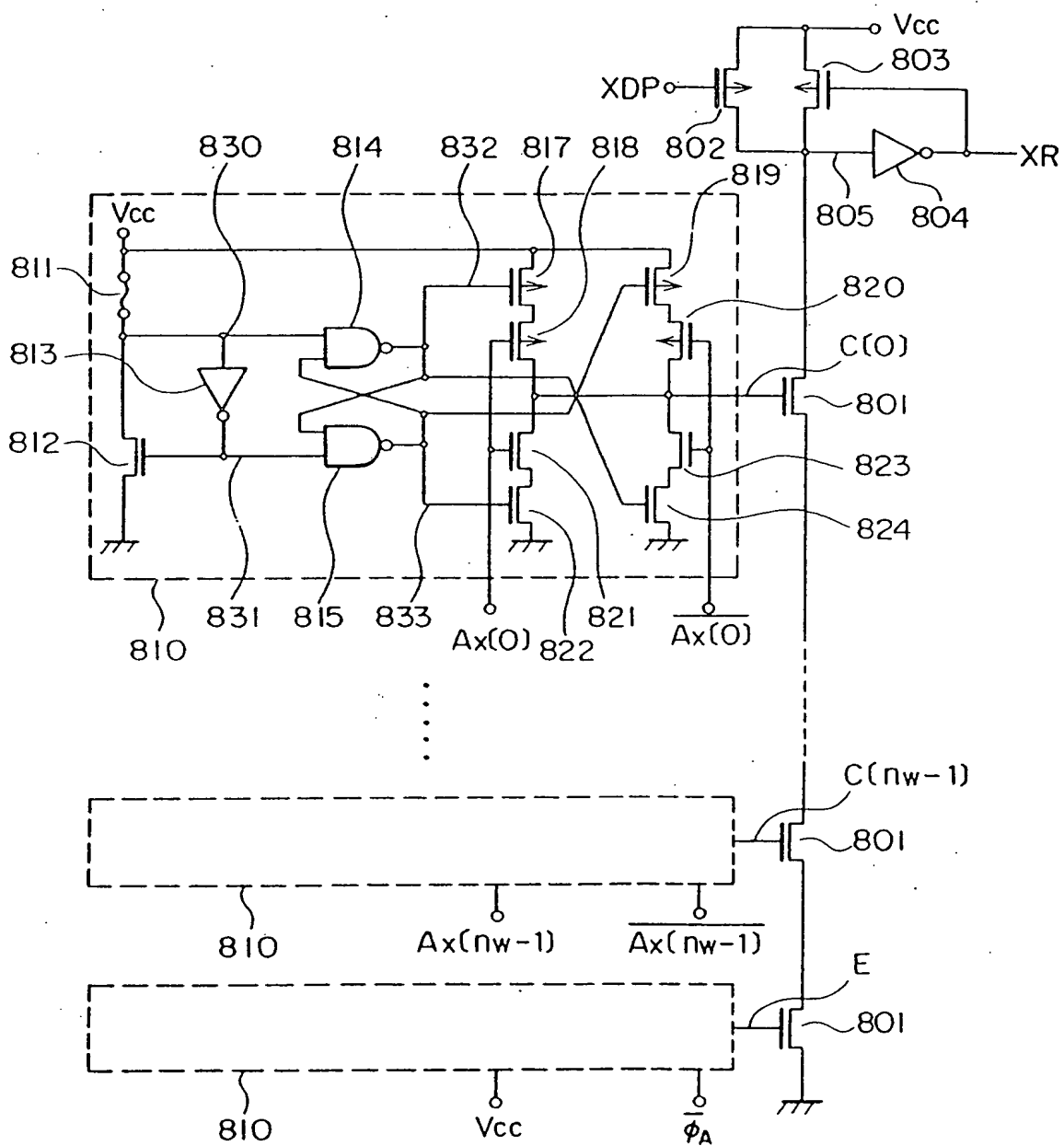


FIG. 20

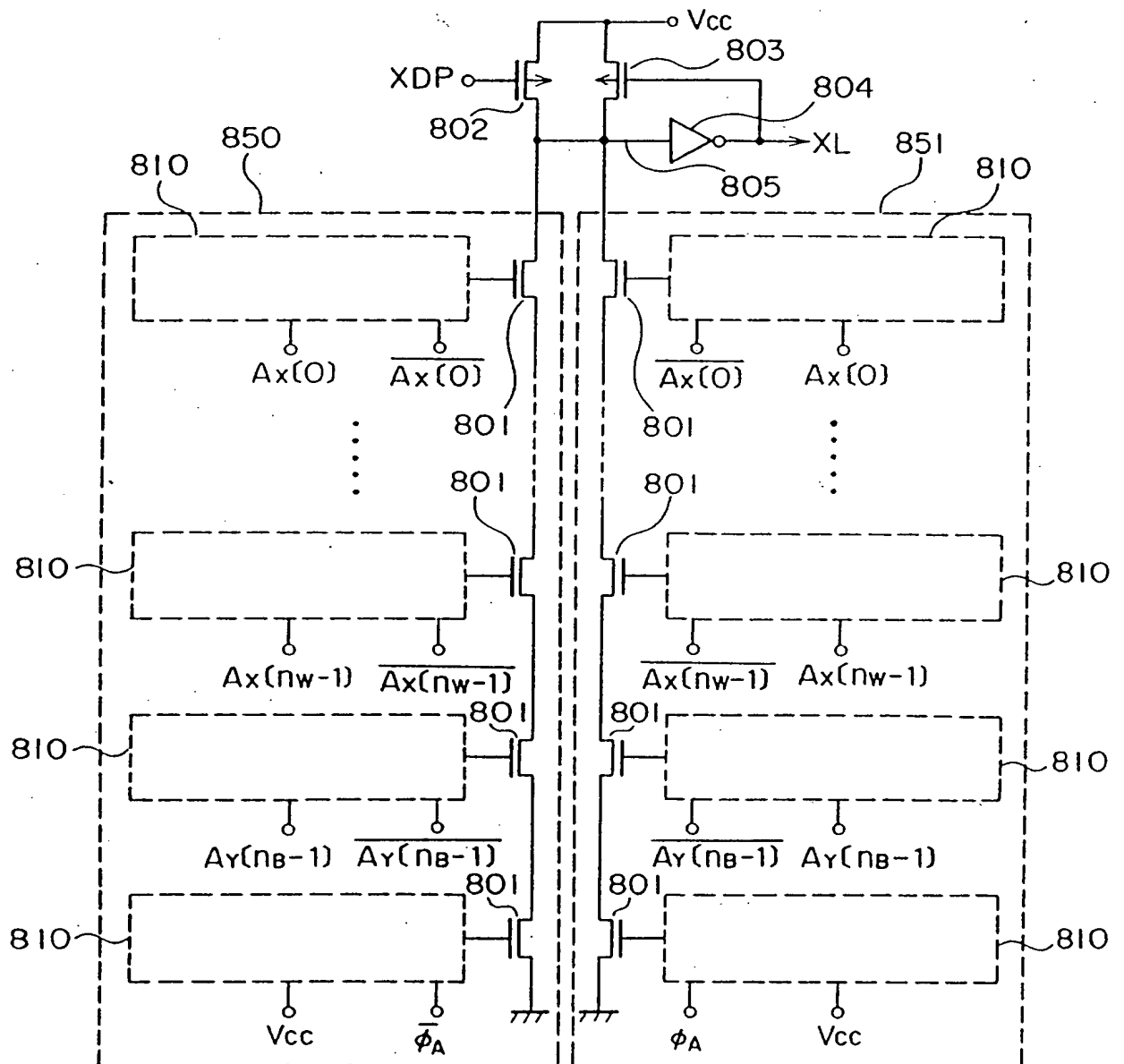


FIG. 21

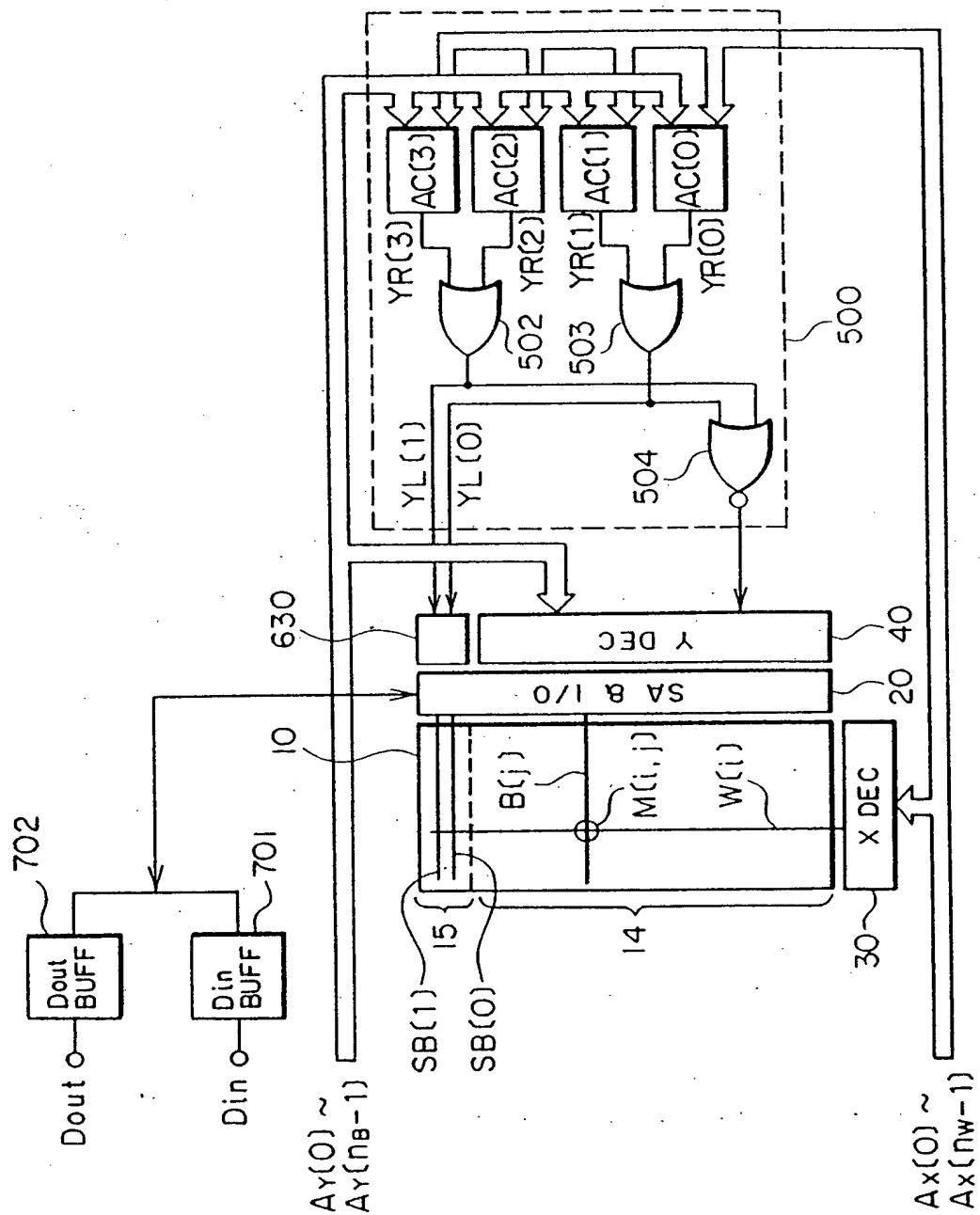


FIG. 22

ADDRESS		COMPARED / NOT COMPARED		
ROW ADDRESS	$A_x(0)$	O	X	X
	$A_x(1)$	O	O	X
	$A_x(2)$	O	O	X
	\vdots	\vdots	\vdots	\vdots
	\vdots	\vdots	\vdots	\vdots
	$A_x(n_w - 3)$	O	O	X
	$A_x(n_w - 2)$	O	O	X
	$A_x(n_w - 1)$	O	O	X
COLUMN ADDRESS	$A_y(0)$	O	O	O
	$A_y(1)$	O	O	O
	$A_y(2)$	O	O	O
	\vdots	\vdots	\vdots	\vdots
	\vdots	\vdots	\vdots	\vdots
	$A_y(n_b - 3)$	O	O	O
	$A_y(n_b - 2)$	O	O	O
	$A_y(n_b - 1)$	O	O	O
NUMBER OF BITS REPLACED AT A TIME		1	2	N_w

O : COMPARED

X : NOT COMPARED

FIG. 23

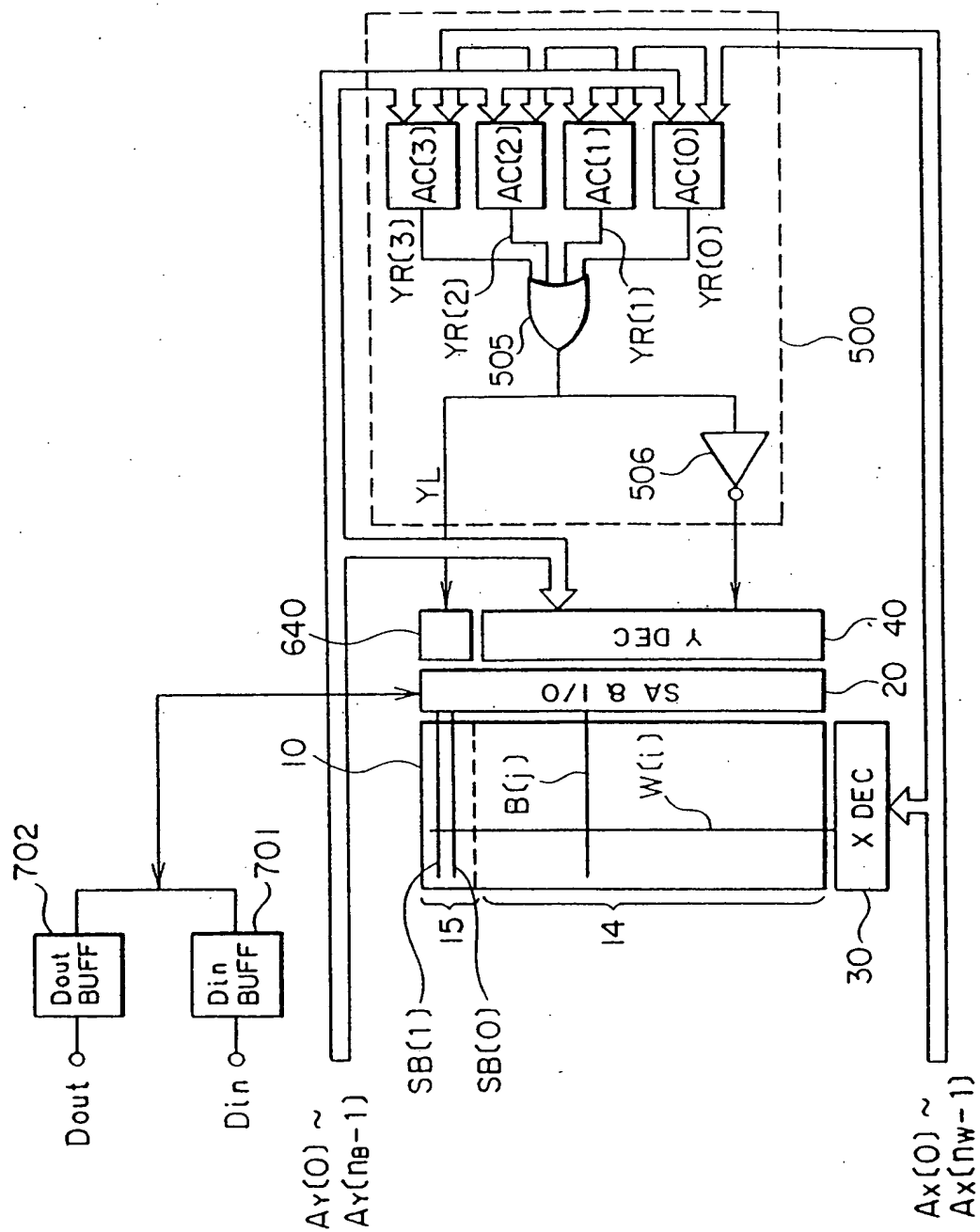


FIG. 24A

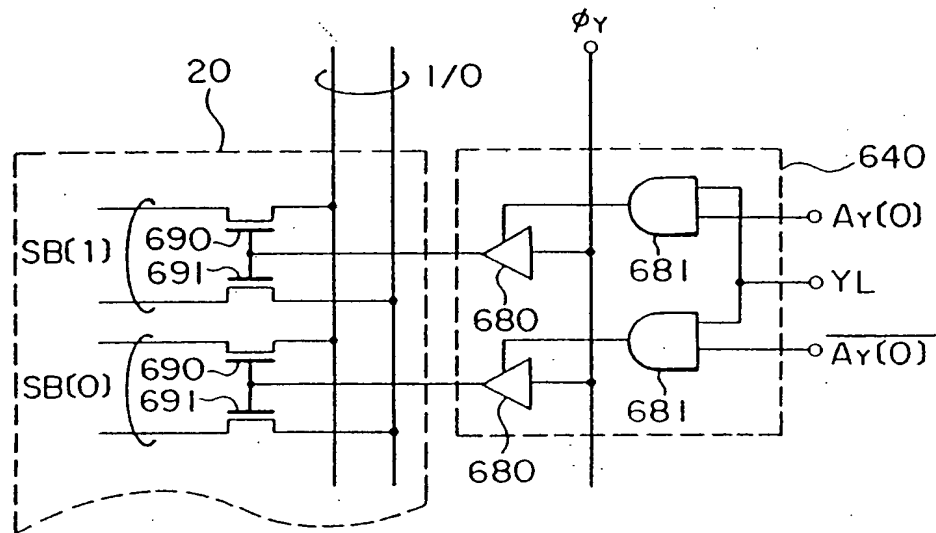


FIG. 24B

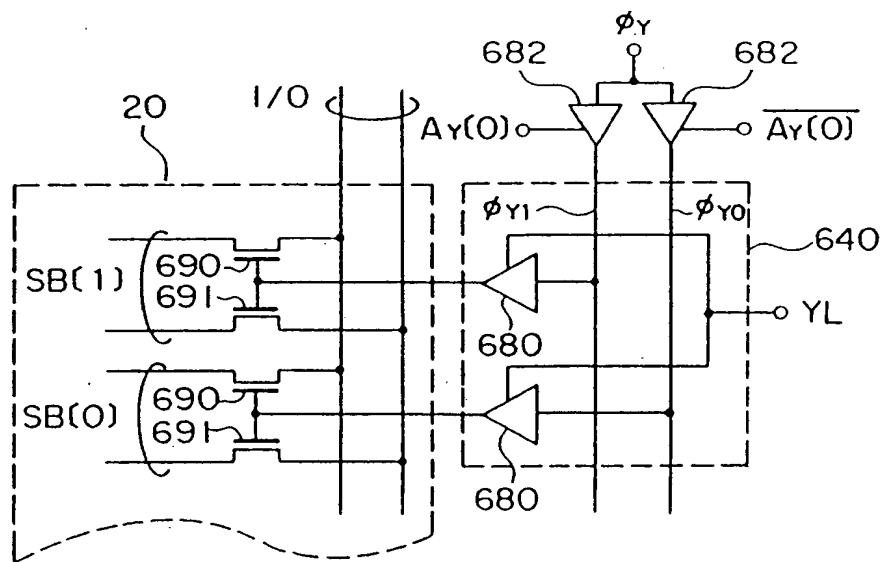


FIG. 25

ADDRESS		COMPARED / NOT COMPARED					
ROW ADDRESS	$A_x(0)$	O	X	O	X	X	X
	$A_x(1)$	O	O	O	O	X	X
	$A_x(2)$	O	O	O	O	X	X
	:	:	:	:	:	:	:
	:	:	:	:	:	:	:
	$A_x(nw-3)$	O	O	O	O	X	X
	$A_x(nw-2)$	O	O	O	O	X	X
	$A_x(nw-1)$	O	O	O	O	X	X
COLUMN ADDRESS	$A_y(0)$	O	O	X	X	O	X
	$A_y(1)$	O	O	O	O	O	O
	$A_y(2)$	O	O	O	O	O	O
	:	:	:	:	:	:	:
	:	:	:	:	:	:	:
	$A_y(nb-3)$	O	O	O	O	O	O
	$A_y(nb-2)$	O	O	O	O	O	O
	$A_y(nb-1)$	O	O	O	O	O	O
NUMBER OF BITS REPLACED AT A TIME		1	2	2	4	Nw	2Nw

O : COMPARED

X : NOT COMPARED

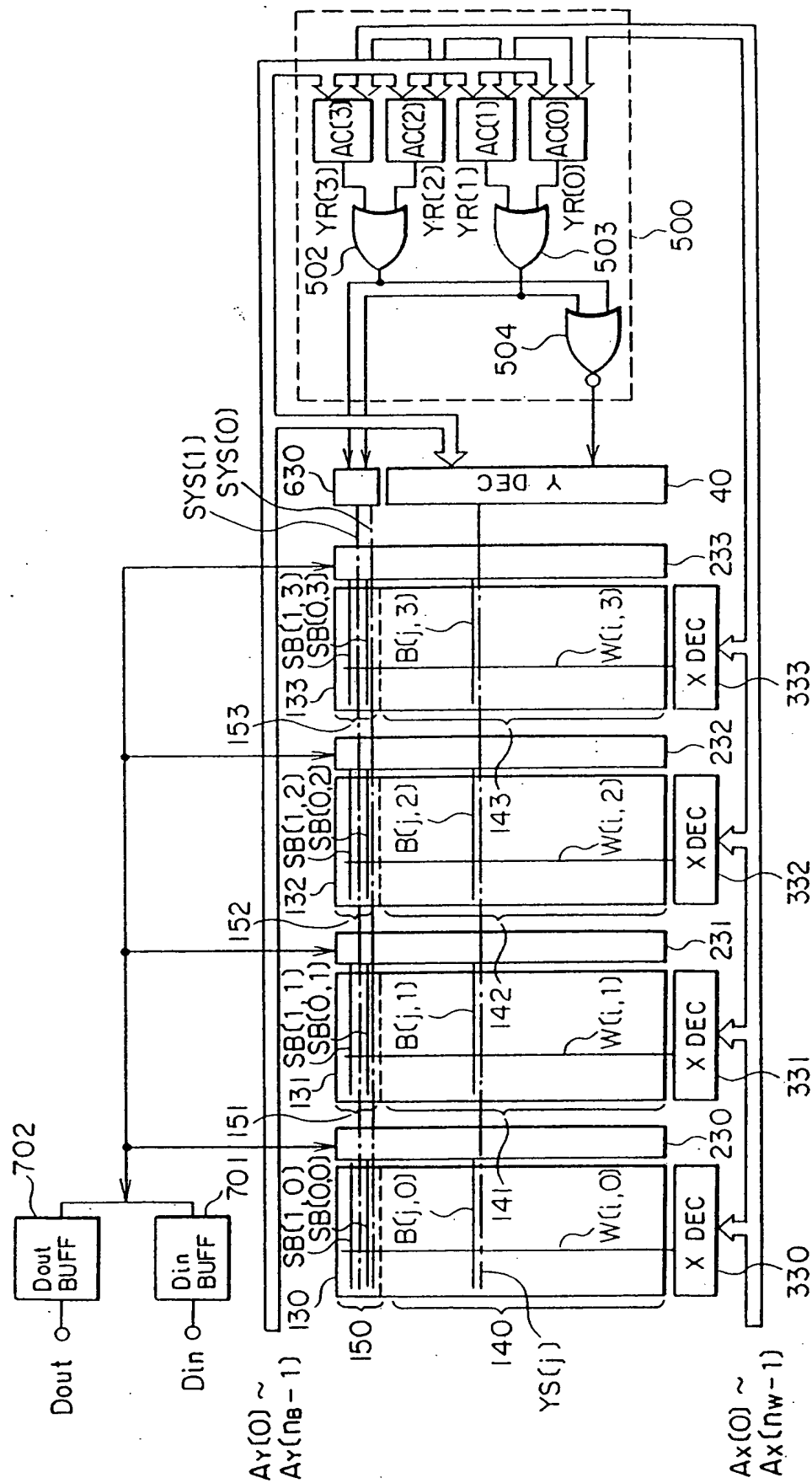
[illegible]

FIG. 27

ADDRESS		COMPARED / NOT COMPARED			
ROW ADDRESS	$A_x(0)$	○	×	×	×
	$A_x(1)$	○	○	×	×
	$A_x(2)$	○	○	×	×
	⋮	⋮	⋮	⋮	⋮
	⋮	⋮	⋮	⋮	⋮
	$A_x(n_w - 3)$	○	○	×	×
	$A_x(n_w - 2)$	○	○	○	×
COLUMN ADDRESS	$A_x(n_w - 1)$	○	○	○	×
	$A_y(0)$	○	○	○	○
	$A_y(1)$	○	○	○	○
	$A_y(2)$	○	○	○	○
	⋮	⋮	⋮	⋮	⋮
	⋮	⋮	⋮	⋮	⋮
	$A_y(n_b - 3)$	○	○	○	○
	$A_y(n_b - 2)$	○	○	○	○
	$A_y(n_b - 1)$	○	○	○	○
NUMBER OF BITS REPLACED AT A TIME		1	2	$\frac{N_w}{4}$	N_w

○ : COMPARED

× : NOT COMPARED

F-1-G-28

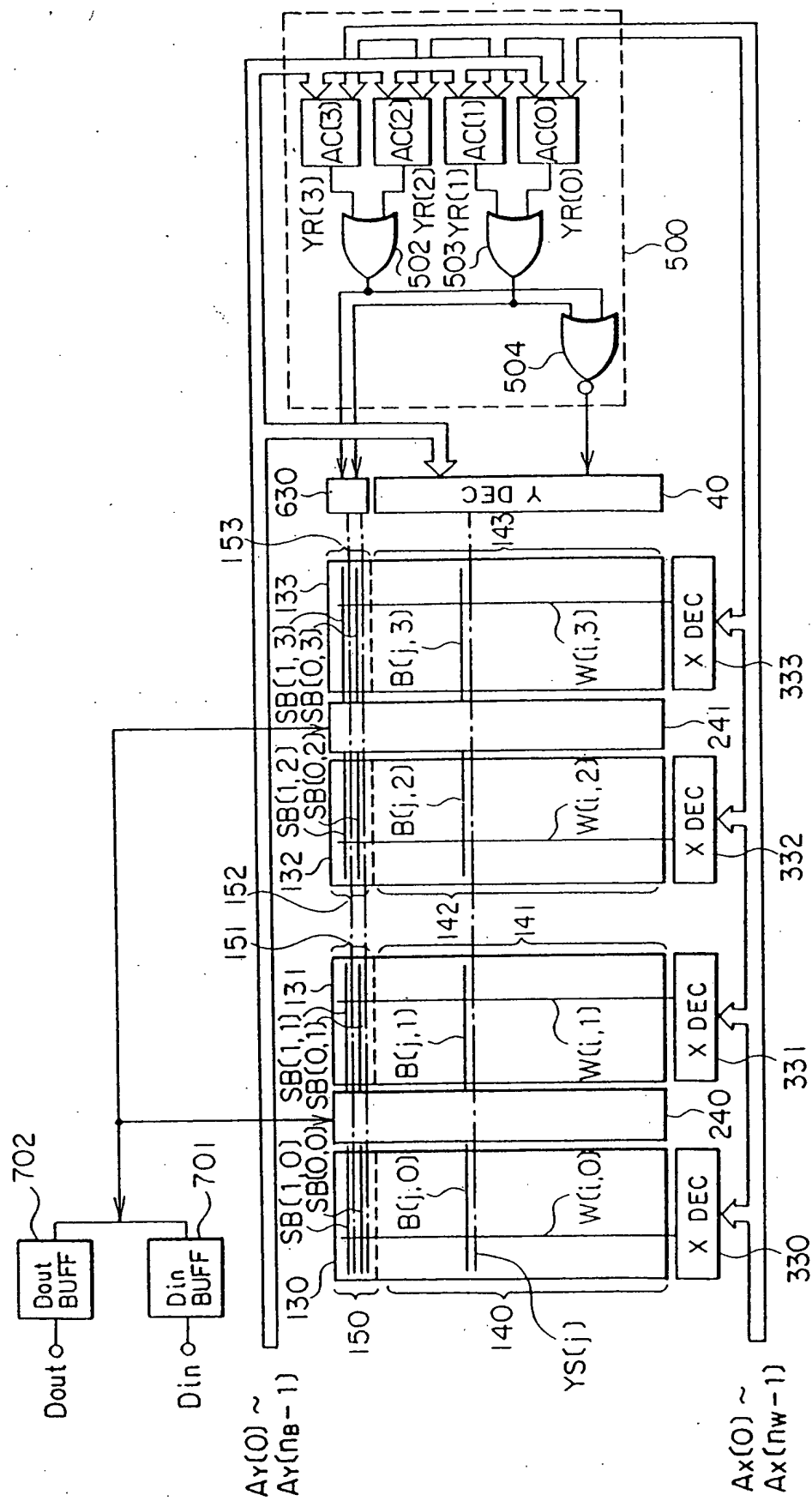


FIG. 29

ADDRESS		COMPARED / NOT COMPARED				
ROW ADDRESS	$A_x(0)$	O	X	X	X	X
	$A_x(1)$	O	O	X	X	X
	$A_x(2)$	O	O	X	X	X
	\vdots	\vdots	\vdots	\vdots	\vdots	\vdots
	\vdots	\vdots	\vdots	\vdots	\vdots	\vdots
	$A_x(n_w - 3)$	O	O	X	X	X
	$A_x(n_w - 2)$	O	O	O	X	X
COLUMN ADDRESS	$A_x(n_w - 1)$	O	O	O	O	X
	$A_y(0)$	O	O	O	O	O
	$A_y(1)$	O	O	O	O	O
	$A_y(2)$	O	O	O	O	O
	\vdots	\vdots	\vdots	\vdots	\vdots	\vdots
	\vdots	\vdots	\vdots	\vdots	\vdots	\vdots
	$A_y(n_b - 3)$	O	O	O	O	O
	$A_y(n_b - 2)$	O	O	O	O	O
	$A_y(n_b - 1)$	O	O	O	O	O
NUMBER OF BITS REPLACED AT A TIME		1	2	$\frac{N_w}{4}$	$\frac{N_w}{2}$	N_w

O : COMPARED

X : NOT COMPARED

FIG. 30

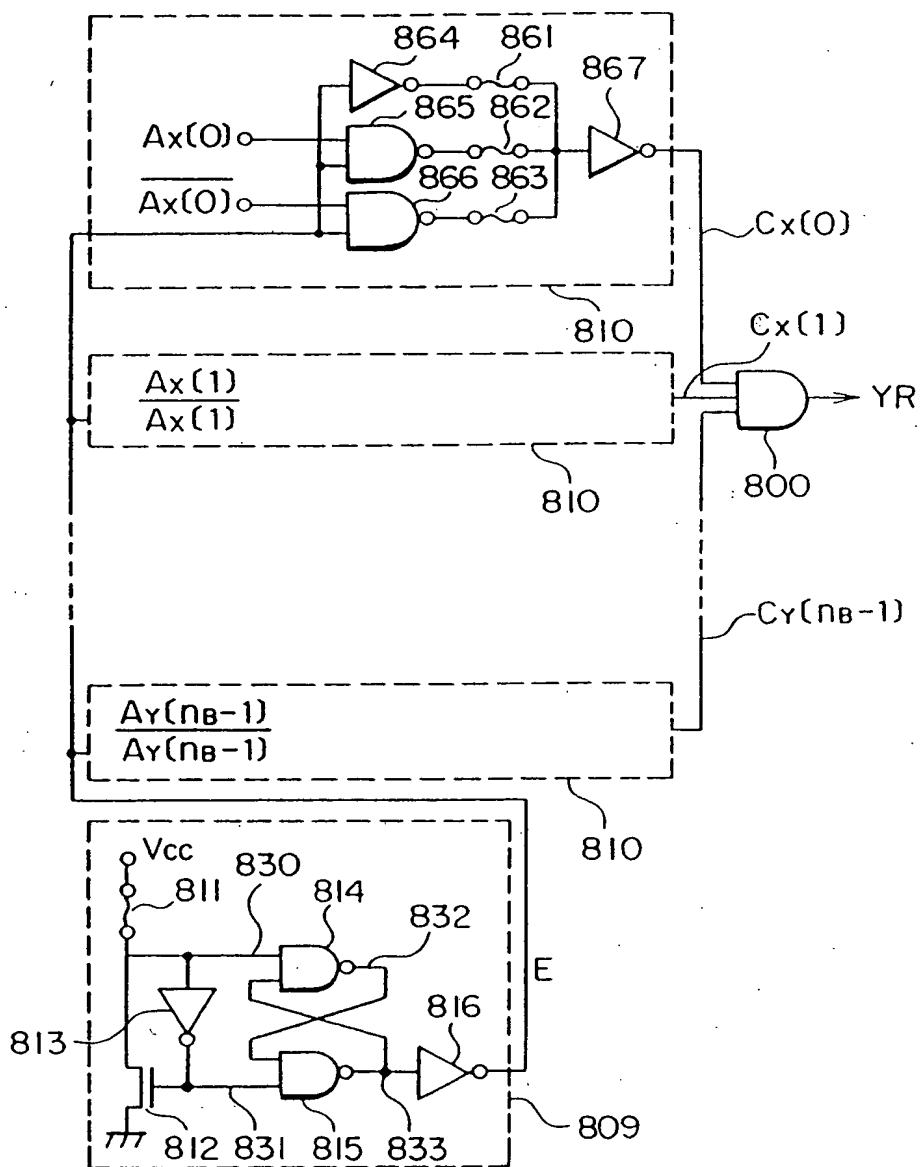


FIG. 31

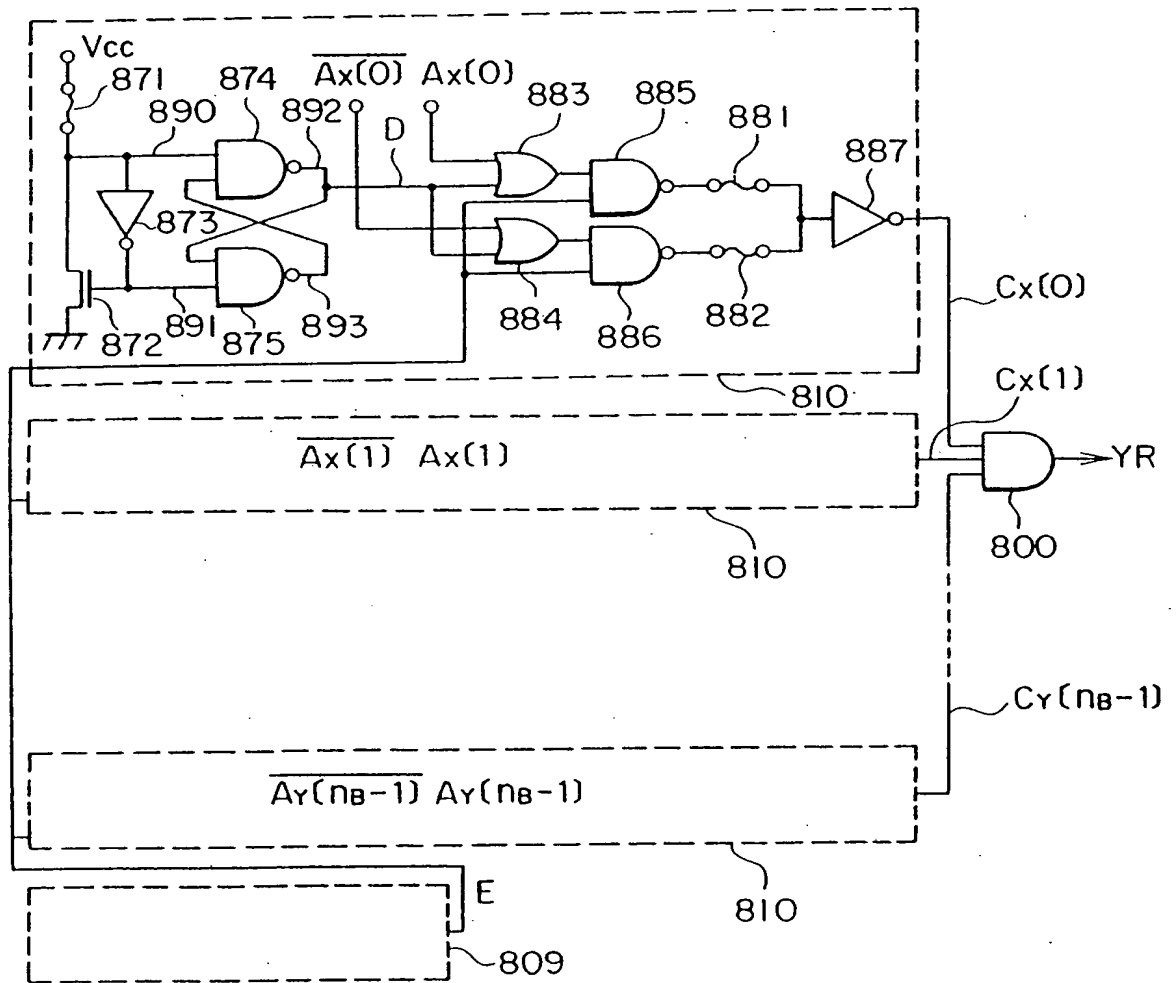


FIG. 32

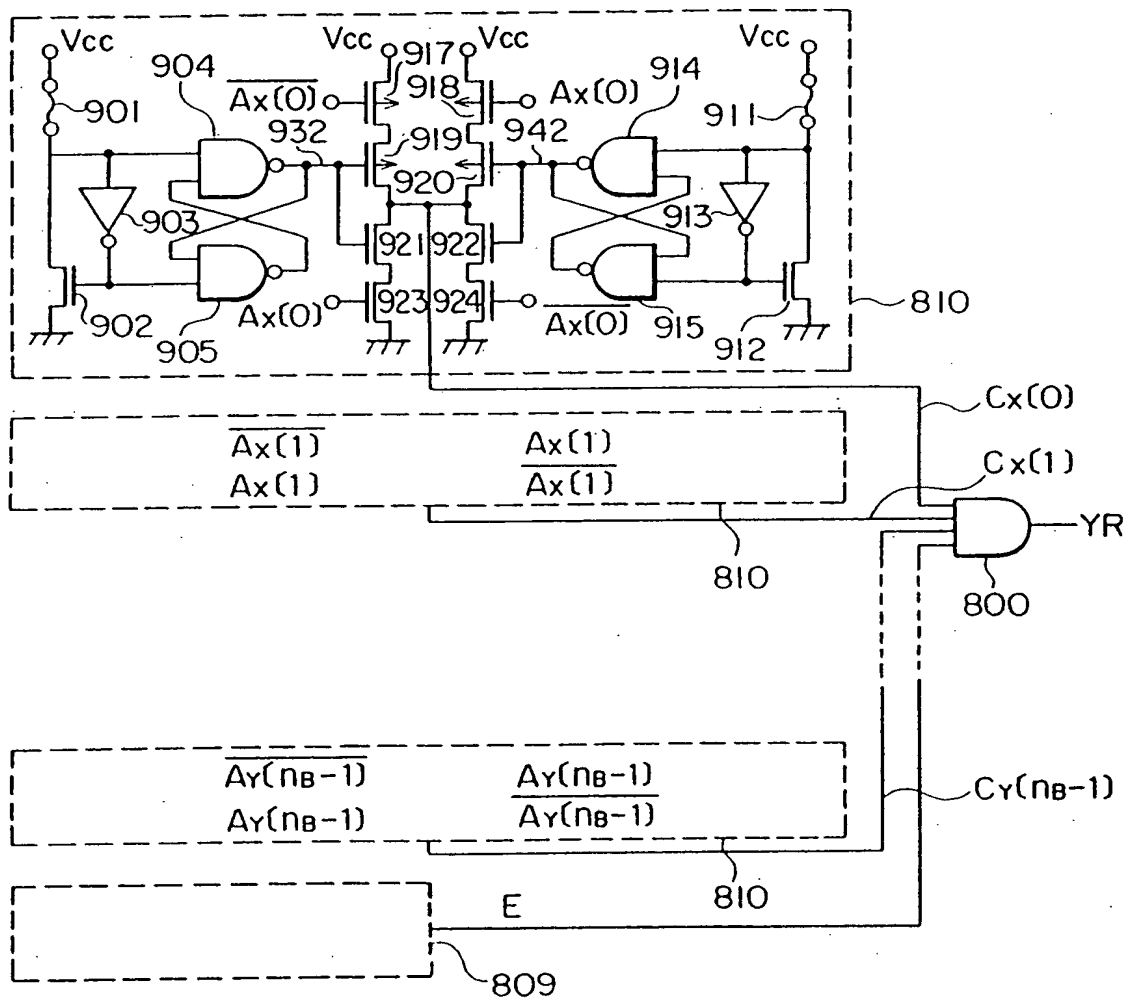


FIG. 33

